

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

DALSON YE SENG KIM  
JEFFREY TOH TUCK FOOK  
LEE CHOON KUAN

SERIAL NO.:

ART UNIT:

FILING DATE: AUGUST 1, 2003

EXAMINER:

FOR: SEMICONDUCTOR COMPONENT HAVING CHIP  
ON BOARD LEADFRAME AND METHOD OF FABRICATION

ATTORNEY DOCKET NO. 02-1467

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.56

August 1, 2003

MAIL STOP PATENT APPLICATION  
Commissioner for Patents

PO Box 1450  
Alexandria, VA 22313-1450

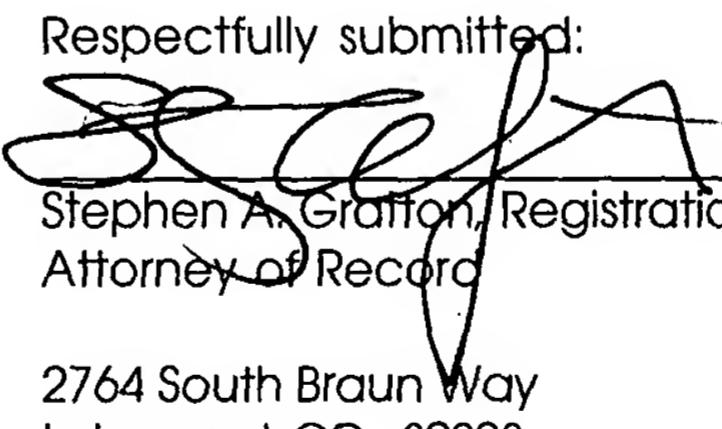
Sir:

The citations listed on the accompanying Form PTO/SB/08, copies attached, may be material to the examination of the subject application and are therefore submitted in compliance with the duty of disclosure defined in 37 CFR §§1.56, 1.97 and 1.98.

This Information Disclosure Statement under 37 CFR §§1.56, 1.97 and 1.98 is not to be construed as a representation that a search has been made, that additional material to the examination of this application does not exist, or that any one or more of these citations constitutes prior art under 35 USC §102.

It is requested that the above citations be made of record, if appropriate, in the prosecution of this application.

Respectfully submitted:

  
Stephen A. Grattan, Registration No. 28,418  
Attorney of Record

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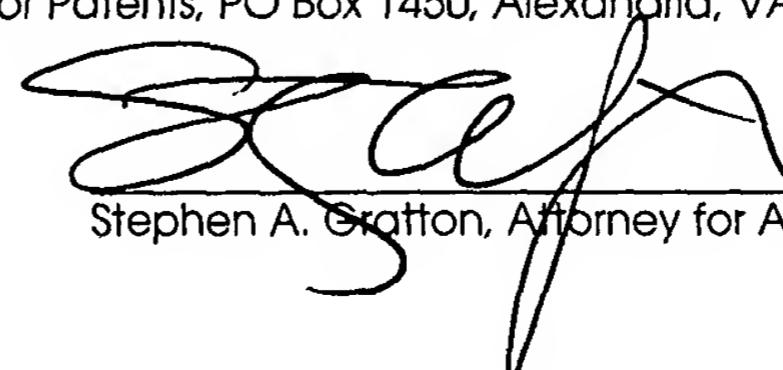
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Stephen A. Grattan, Attorney for Applicants

Substitute for form 1449/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

1

of 3

## Complete if Known

Application Number	
Filing Date	August 1, 2003
First Named Inventor	DALSON YE SENG KIM
Art Unit	
Examiner Name	

Attorney Docket Number 02-1467

## U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
A	US- 5,216,278		06/01/1993	Lin et al.	
B	US- 5,397,921		03/14/1995	Karnezos	
C	US- 5,409,865		04/25/1995	Karnezos	
D	US- 5,420,460		05/30/1995	Massingill	
E	US- 5,581,226		12/03/1996	Shah	
F	US- 5,663,593		09/02/1997	Mostafazadeh et al.	
G	US- 5,866,939		02/02/1999	Shin et al.	
H	US- 5,898,220		04/27/1999	Ball	
I	US- 5,969,416		10/19/1999	Kim	
J	US- 5,973,393		10/26/1999	Chia et al.	
K	US- 5,976,912		11/02/1999	Fukutomi et al.	
L	US- 6,028,356		02/22/2000	Kimura	
M	US- 6,064,111		05/16/2000	Sota et al.	
N	US- 6,083,776		07/04/2000	Manteghi	
O	US- 6,114,760		09/05/2000	Kim et al.	
P	US- 6,181,000B1		01/30/2001	Ooigawa et al.	
Q	US- 6,187,612B1		02/13/2001	Orcutt	
R	US- 6,228,683B1		05/08/2001	Manteghi	
S	US- 6,310,390B1		10/30/2001	Moden	

## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.

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First Named Inventor

DALSON YE SENG KIM

Art Unit

Examiner Name

02-1467

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	Z	MASASUMI AMAGAI, "Chip-Scale Packages for Center-Pad Memory Devices", CHIP SCALE REVIEW, May 1998, pgs. 68-72.	
	AA	MASASUMI AMAGAI, "CSP with Flexible Substrates-Packaging Process", CHIP SCALE REVIEW, May, 1998, pgs 73-77.	
	AB	US Patent Application Serial No. 10/136,186, filed May 1, 2002.	

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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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